

Small quantum algorithms realized in an ion trap array

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Outline:

- Ion traps in a nutshell
- NIST multi-trap architecture
- Multi - qubit algorithms
- Outlook



ARDA



NIST

National Institute of Standards and Technology
Technology Administration, U.S. Department of Commerce

NIST quantum logic group

(April 26 2005)



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John Chiaverini (postdoc, Stanford)

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John Jost (grad student, CU)

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Rainer Reichle (postdoc, Freiburg)

Till Rosenband (grad student, CU)



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Signe Seidelin (postdoc, Orsay)

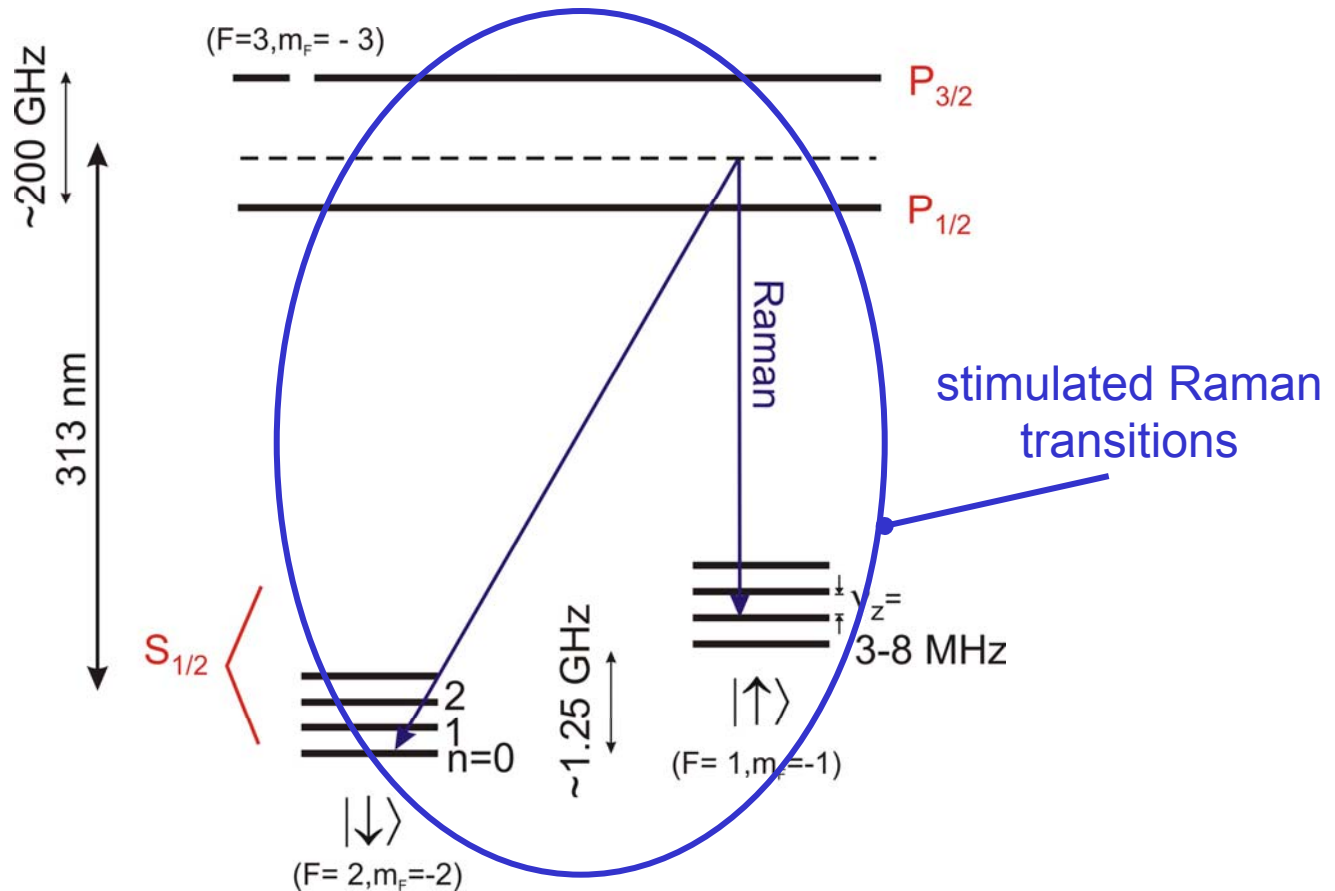
Janus Wesenberg (postdoc, Aarhus)

David Wineland

DiVincenzo requirements

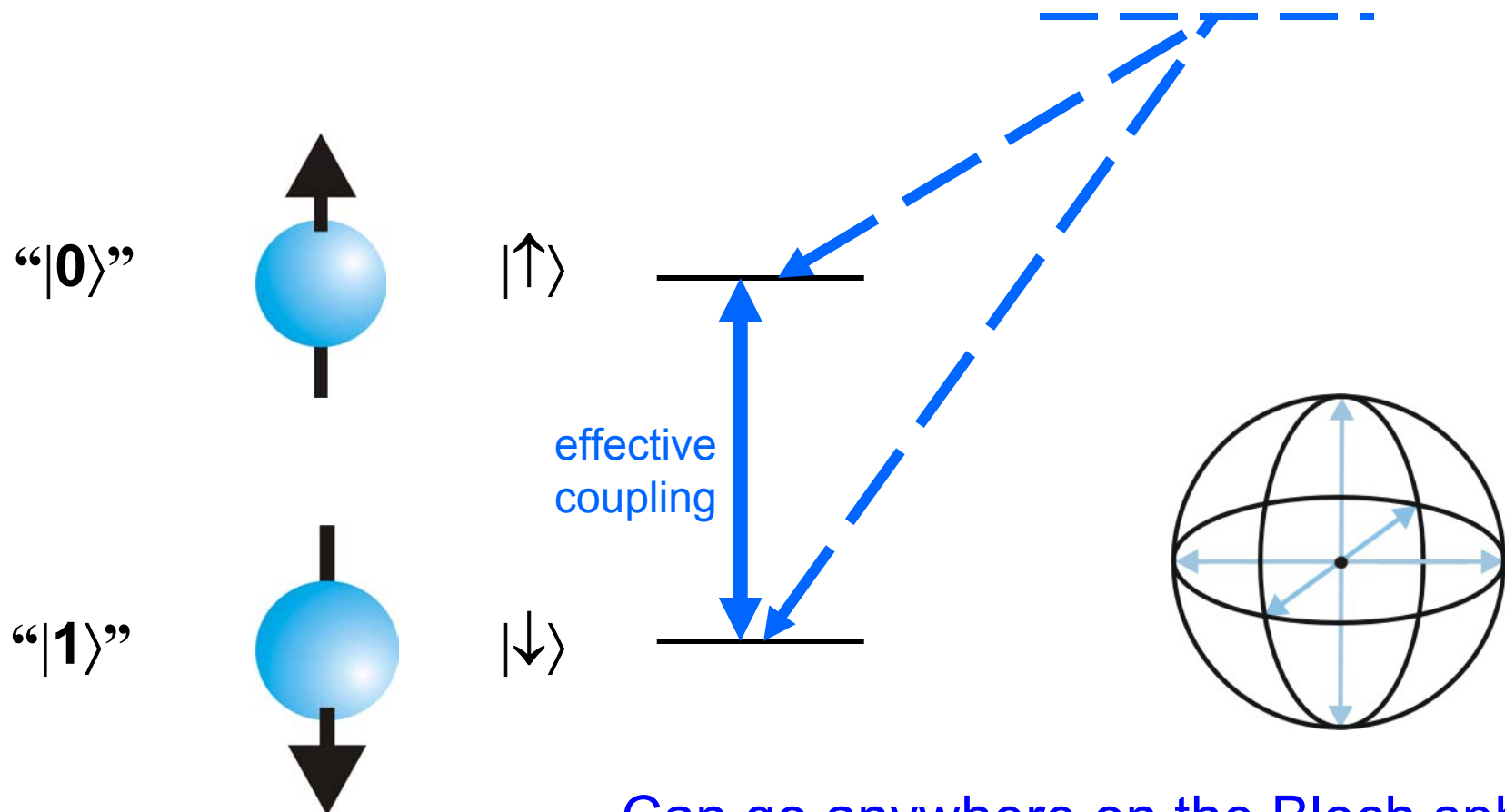
- I. A scalable physical system with well characterized qubits
- II. The ability to initialize the state of the qubits to a simple fiducial state
optical pumping, ground-state cooling (99.9%) 
- III. Long relevant decoherence times, much longer than the gate time
Hyperfine ground states $T_{\text{dec}} > 10 \text{ sec}$ demonstrated 
- IV. A universal set of quantum gates (single qubit rot., two qubit gate)
- V. A qubit-specific read out capability

The qubit: ${}^9\text{Be}^+$



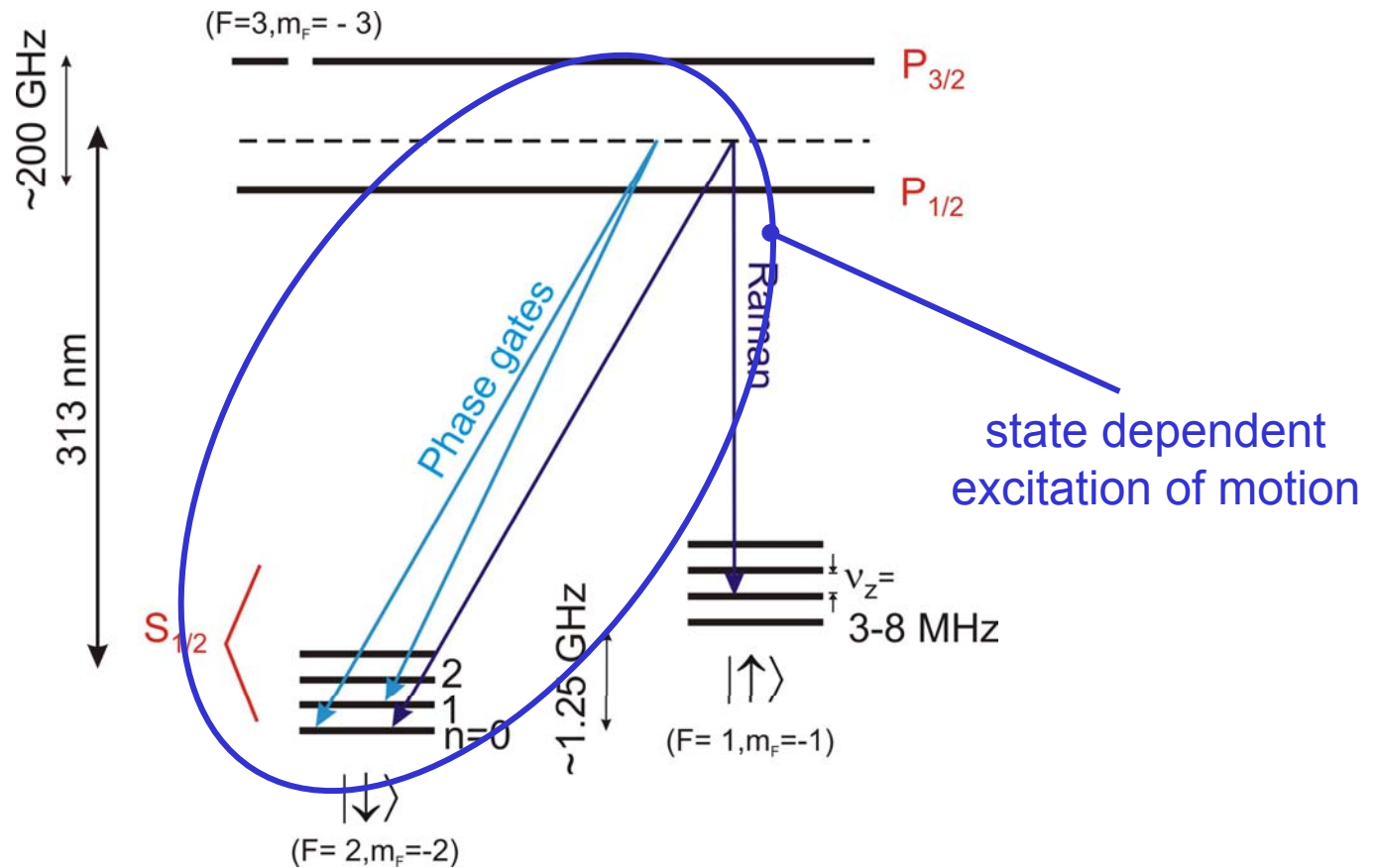
>10 sec decoherence time

Single qubit rotation: stimulated Raman transitions

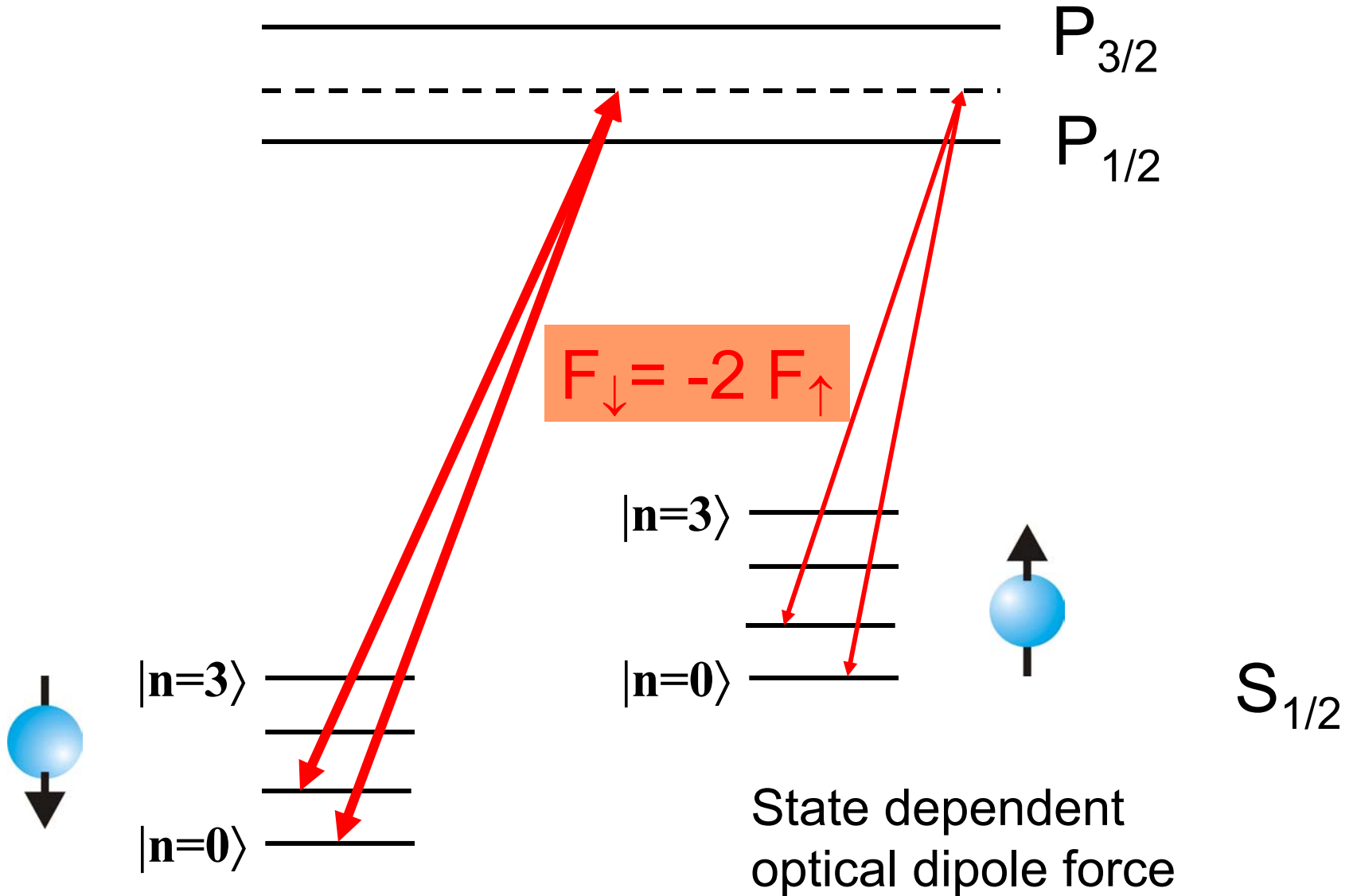


Can go anywhere on the Bloch sphere
Typical π -pulse time: $1 \mu\text{s}$ ($Q=10^7$)
Fidelity: $>99.5\%$

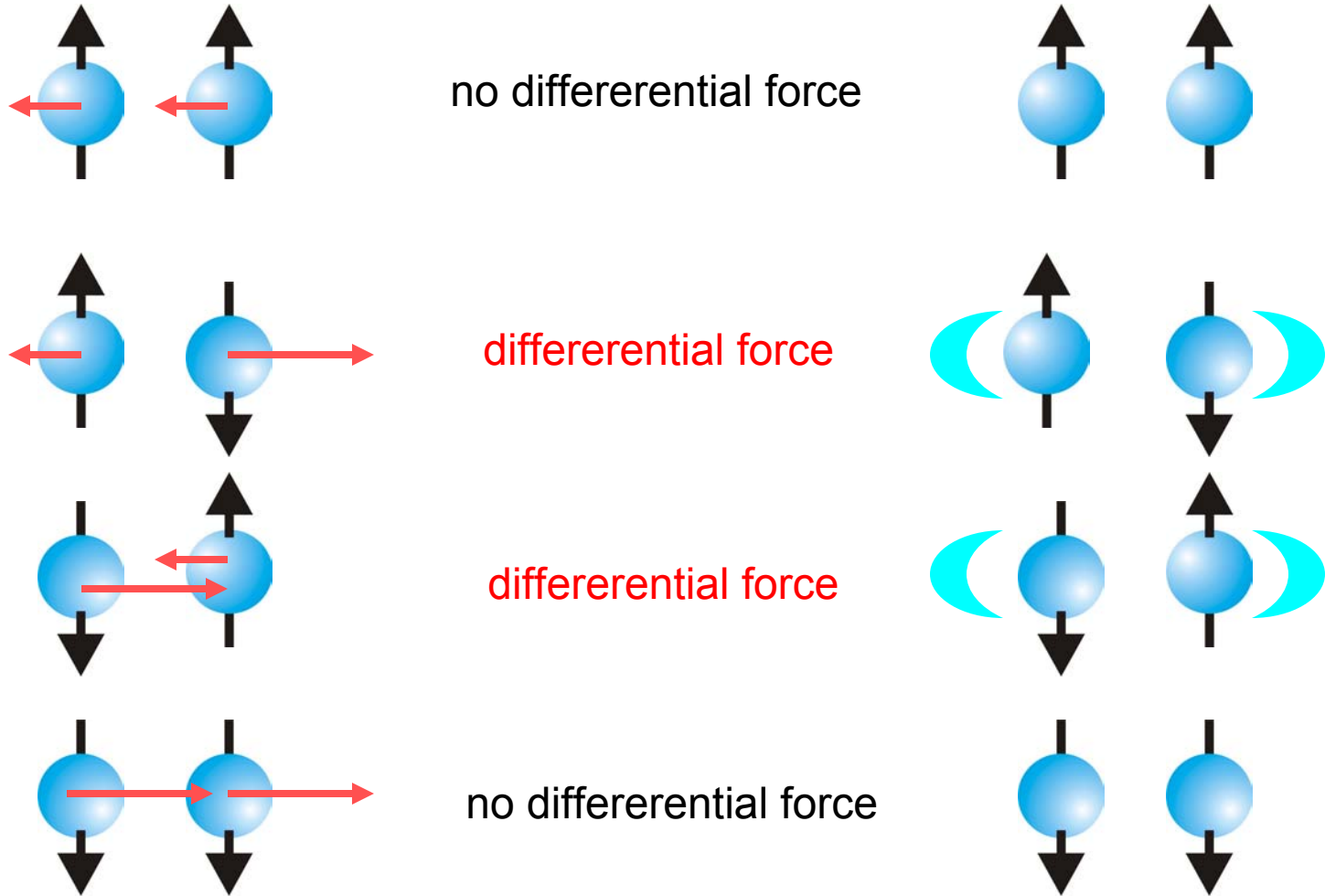
The qubit: ${}^9\text{Be}^+$



Motional excitation



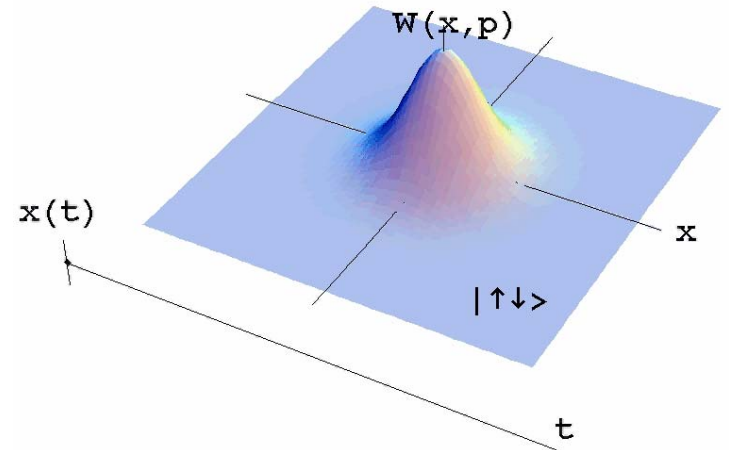
Stretch mode excitation



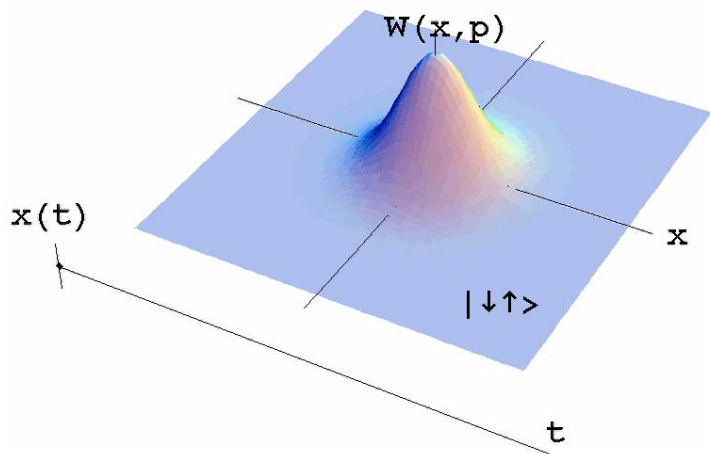
Phase space picture

Gate fidelity: 97%
 Gate time: $7 \mu\text{s}$ (ca. $25/v_{\text{COM}}$)

D. Leibfried *et al.*, Nature **422**, 414 (2003)
 Theory: Milburn, Sørensen & Mølmer



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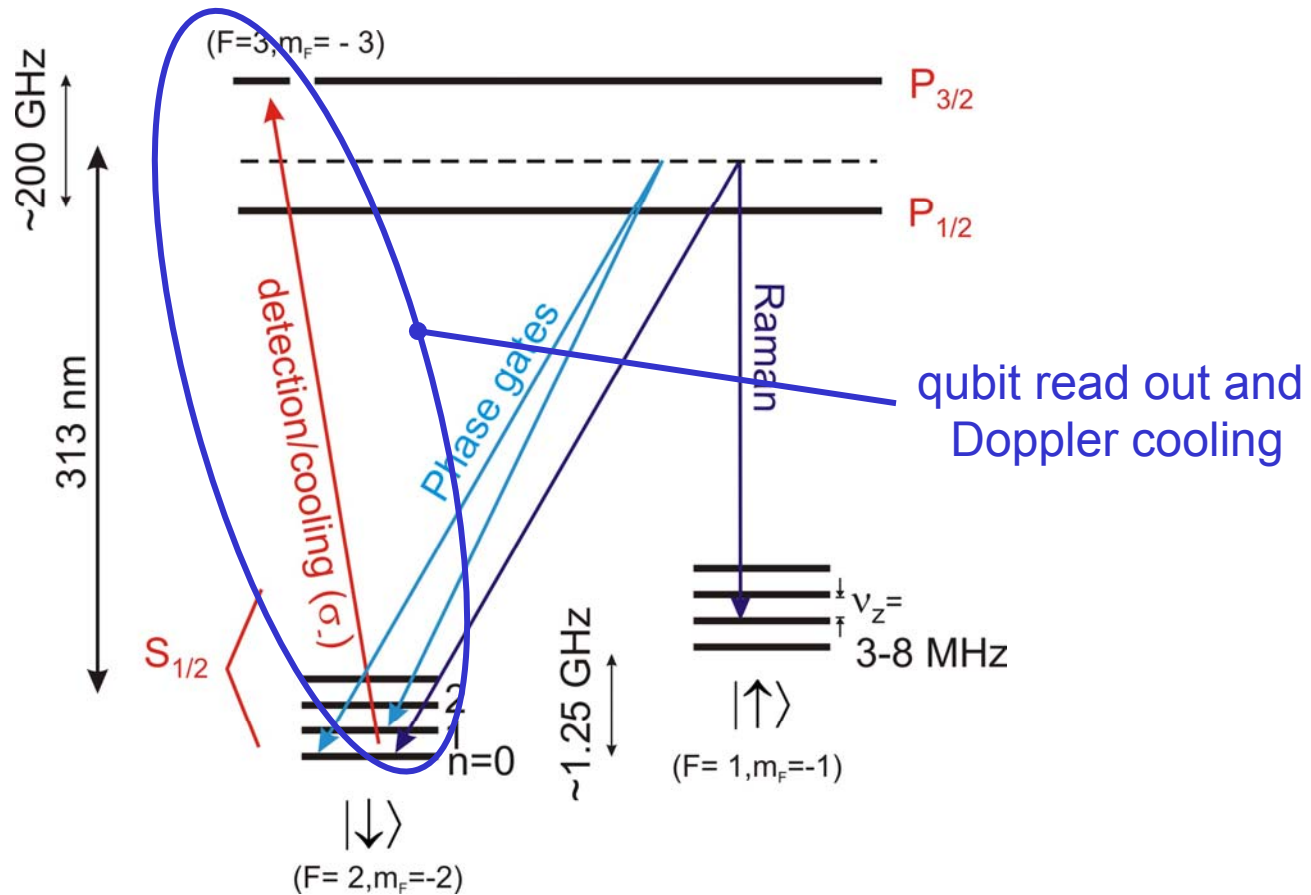


$$\begin{aligned}
 |\downarrow\downarrow\rangle &\rightarrow |\downarrow\downarrow\rangle \\
 |\downarrow\uparrow\rangle &\rightarrow e^{i\phi} |\downarrow\uparrow\rangle \\
 |\uparrow\downarrow\rangle &\rightarrow e^{i\phi} |\uparrow\downarrow\rangle \\
 |\uparrow\uparrow\rangle &\rightarrow |\uparrow\uparrow\rangle
 \end{aligned}$$

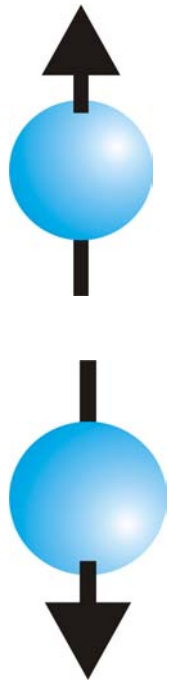
DiVincenzo requirements

- I. A scalable physical system with well characterized qubits
- II. The ability to initialize the state of the qubits to a simple fiducial state
optical pumping, ground-state cooling ✓
- III. Long relevant decoherence times, much longer than the gate time
Hyperfine ground states $T_{\text{dec}} > 10 \text{ sec}$ demonstrated ✓
- IV. A universal set of quantum gates (single qubit rot., two qubit gate)
co-carrier rotations, phase gate ✓
- V. A qubit-specific read out capability

The qubit: ${}^9\text{Be}^+$



Qubit readout: electron shelving



$|\uparrow\rangle$



$|\downarrow\rangle$



$|\mathbf{P}_{3/2} \mathbf{F}=3, m_{\mathbf{F}}=-3\rangle$

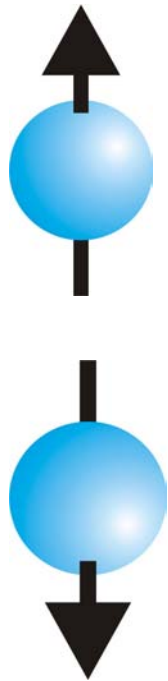


Qubit readout

Electron shelving
(Dehmelt)

$$|P_{3/2} \mathbf{F}=3, m_F=-3\rangle$$

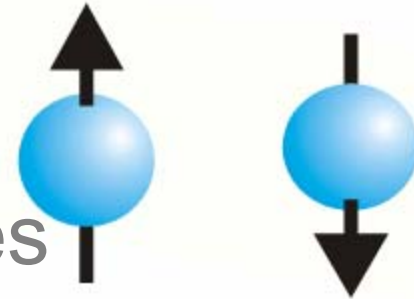
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$|\uparrow\rangle$



$|\downarrow\rangle$



Be⁺ (NIST):

200 μs detection period

10 counts in $|\downarrow\rangle$

0.1 counts in $|\uparrow\rangle$

>99% state discrimination

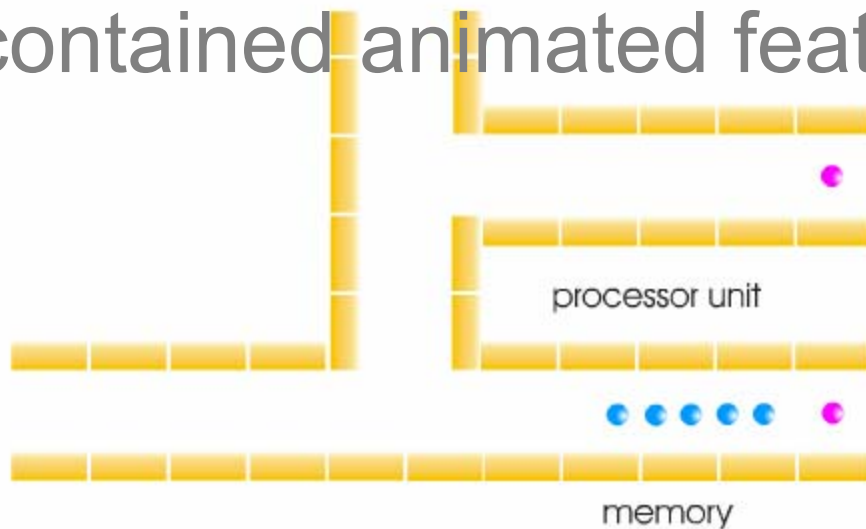
DiVincenzo requirements

- I. A **scalable** physical system with well characterized qubits
- II. The ability to initialize the state of the qubits to a simple fiducial state
optical pumping, ground-state cooling (99.9%) $\Rightarrow |\downarrow\downarrow\downarrow\downarrow\dots\rangle |0\rangle$ ✓
- III. Long relevant decoherence times, much longer than the gate time
 $T_{\text{dec}} > 10 \text{ s}$, $T_{\text{gate}} = 10 \mu\text{s}$, heating irrelevant ✓
- IV. A universal set of quantum gates (single qubit rot., two qubit gate)
co-carrier rotations, geometric-phase gate, heating tolerable ✓
- V. A qubit-specific read out capability
electron shelving method, 99% readout efficiency (100%) ✓

NIST Multiplexed trap architecture

extension of J. I. Cirac and P. Zoller, PRL **74**, 4091 (1995).

This viewgraph originally contained animated features



interconnected multi-trap structure
subtraps completely decoupled

routing of ions by controlling
electrode voltages

processor sympathetically cooled
only need to cool three normal modes
no need for ground state cooling
in memory

no individual optical addressing
during two-qubit gates
can do gates in tight trap

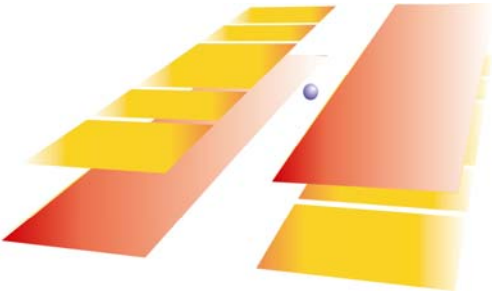
one-qubit gates in extra subtrap
ion is strongly confined and
easily addressed

readout in extra subtrap
no rescattering of fluorescence

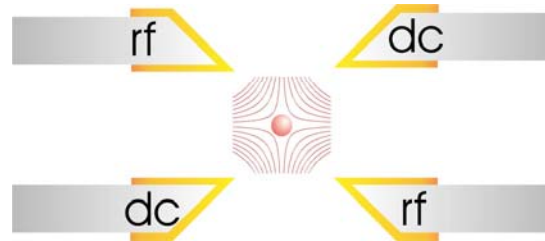
D. J. Wineland, *et al.*,
J. Res. Nat. Inst. Stand. Technol. **103**, 259 (1998);
D. Kielpinski, C. Monroe, and D. J. Wineland,
Nature **417**, 709 (2002).
Other proposals: DeVoe, Phys. Rev. A **58**, 910 (1998) .
Cirac & Zoller, Nature **404**, 579 (2000) .

Two layer Paul trap

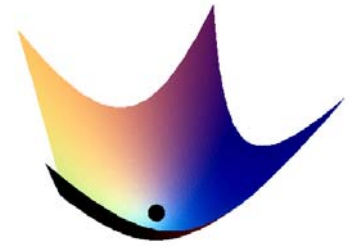
radial confinement:



radial cross section



ac quadrupole field



harmonic time averaged
pseudo-potential

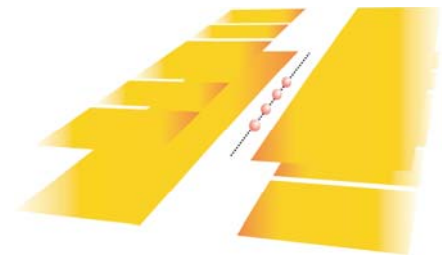
axial confinement:



axial cross section



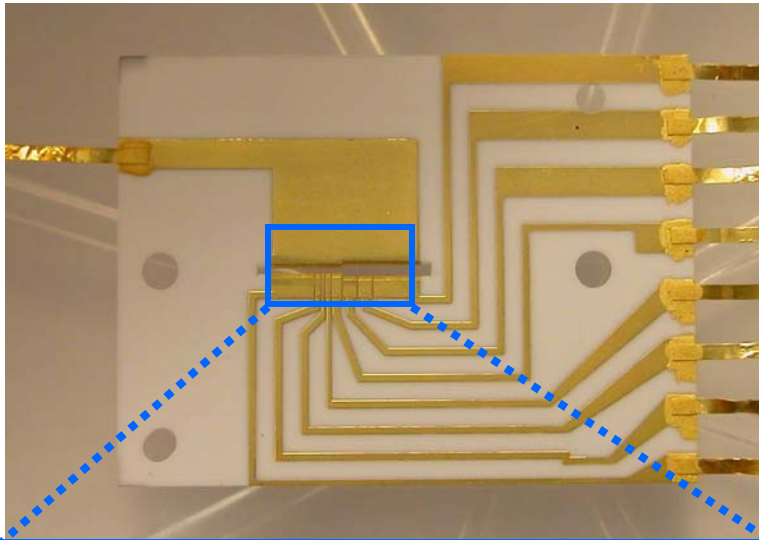
static harmonic axial potential



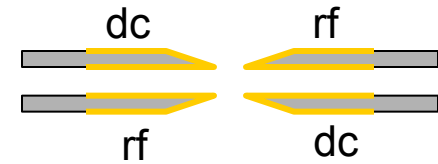
radial conf. \gg axial conf.
 \Rightarrow ions align along trap axis

This viewgraph originally
contained animated features

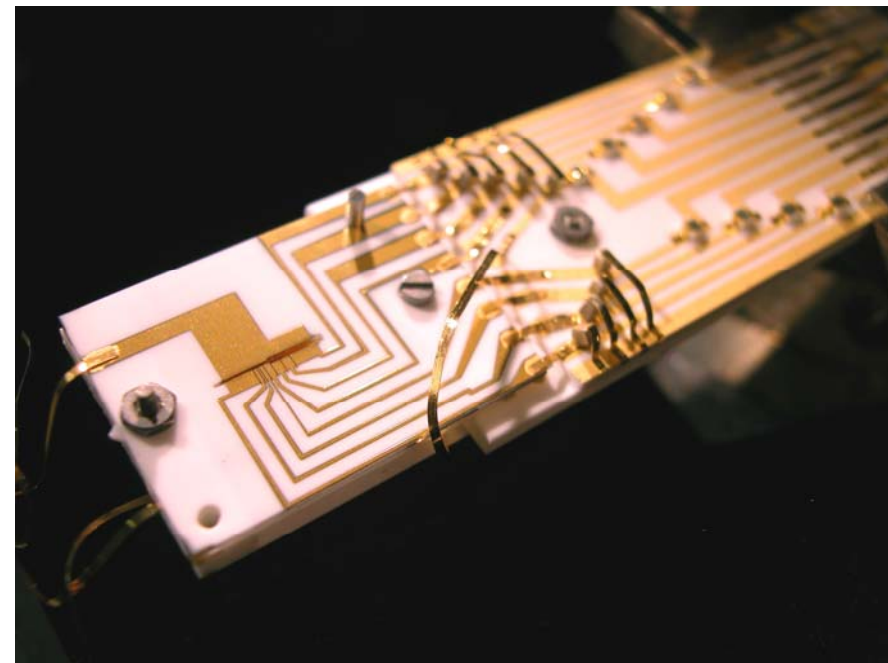
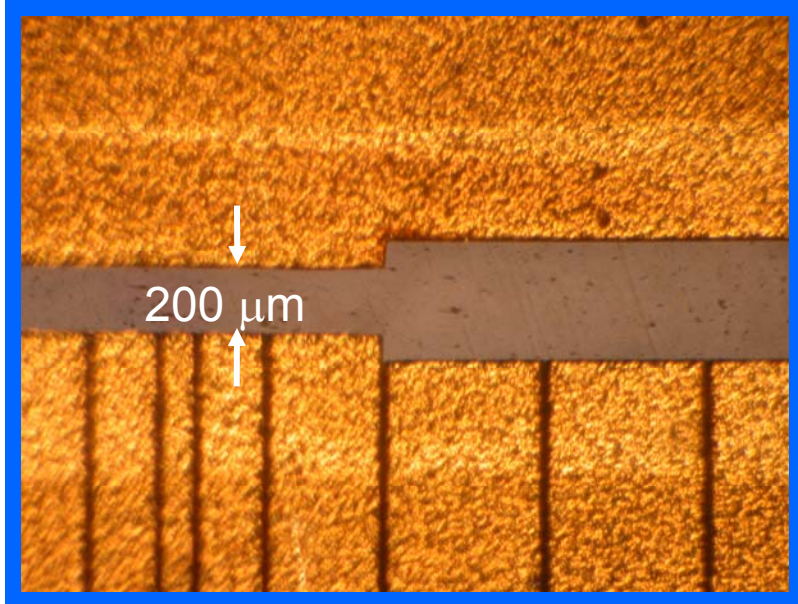
Two Layer Trap Technology



2 wafers of alumina (0.2 mm thick)
gold conducting surfaces (2 μm)



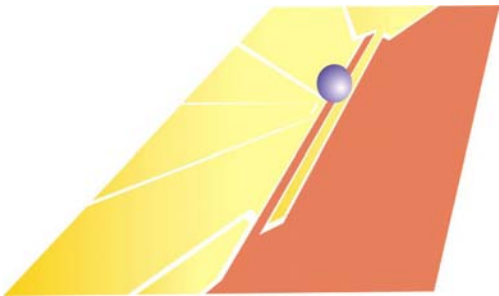
6 zones, dedicated loading zone



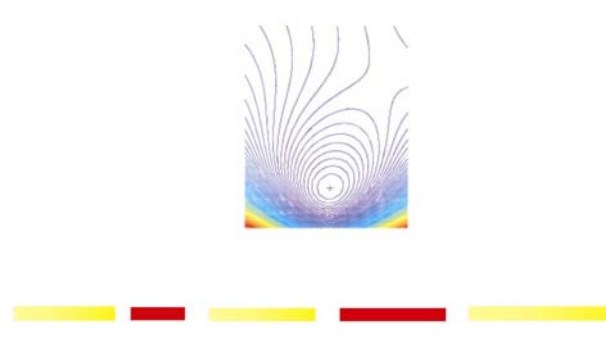
Murray Barrett/John Jost

Surface trap

radial confinement:



radial cross section

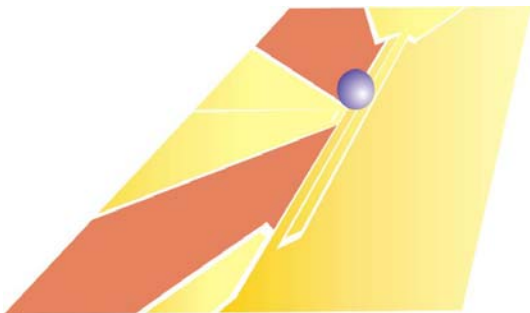


time averaged pseudo-potential

axial cross section



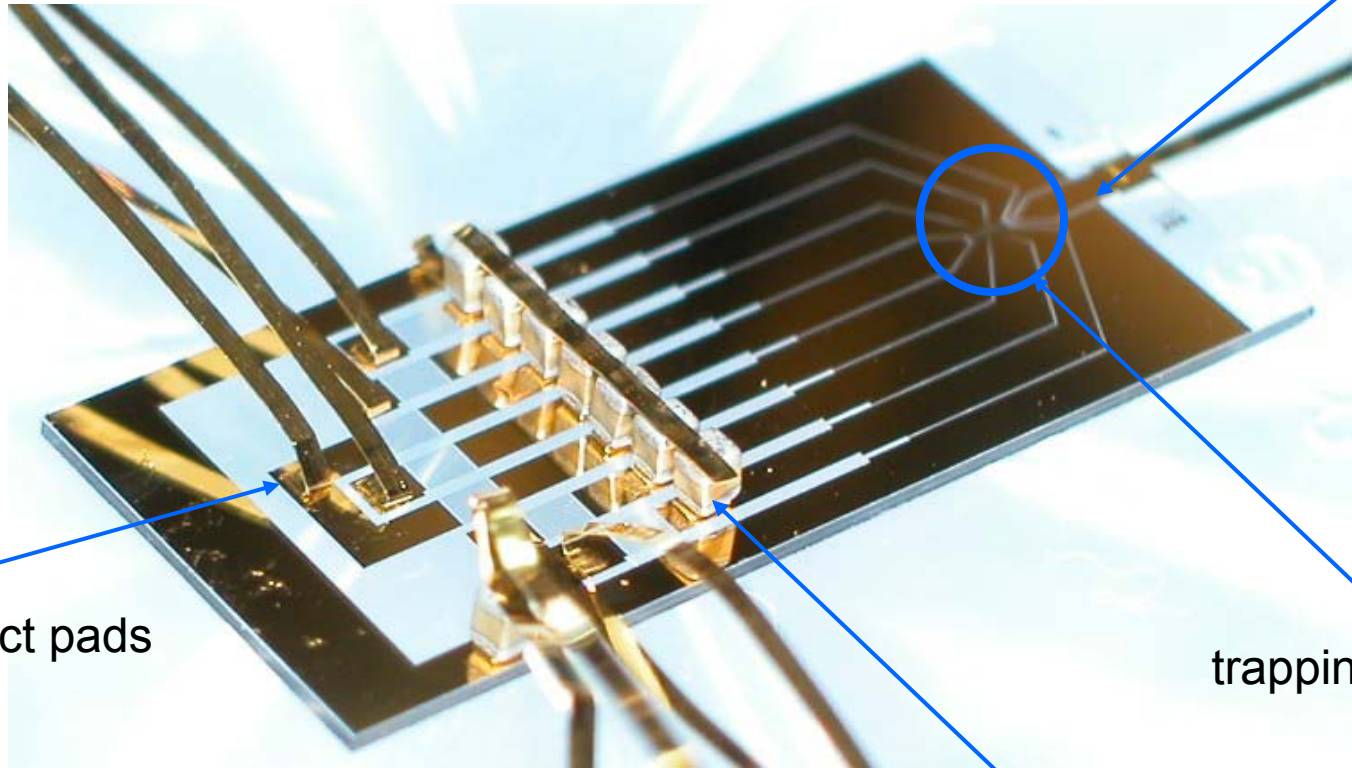
axial confinement:



Planar Trap Chip

Gold on fused silica

RF



DC Contact pads

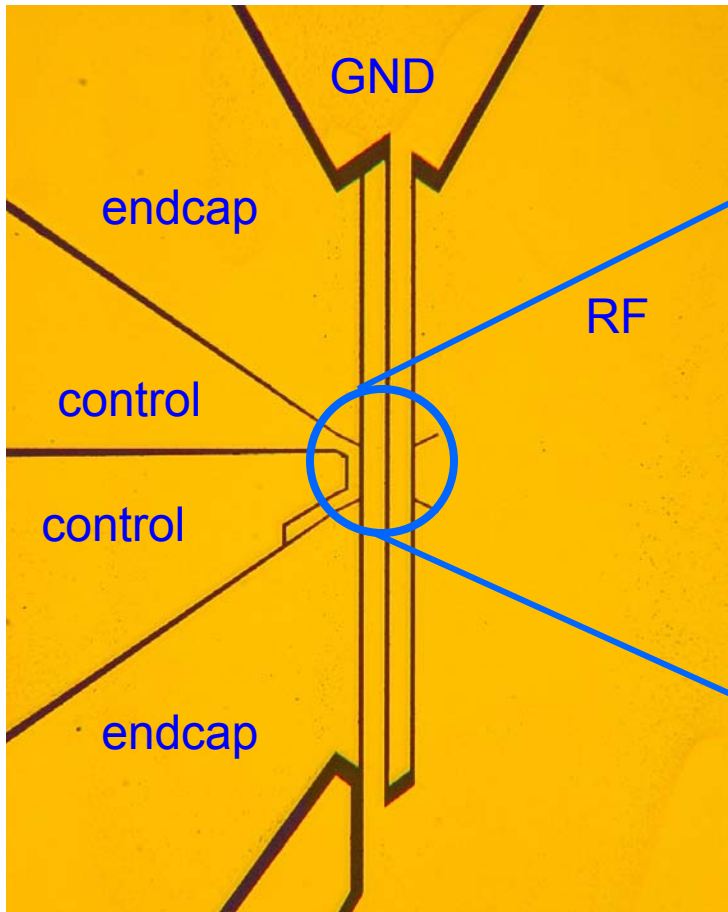
trapping region

John Chiaverini, Signe Seidelin

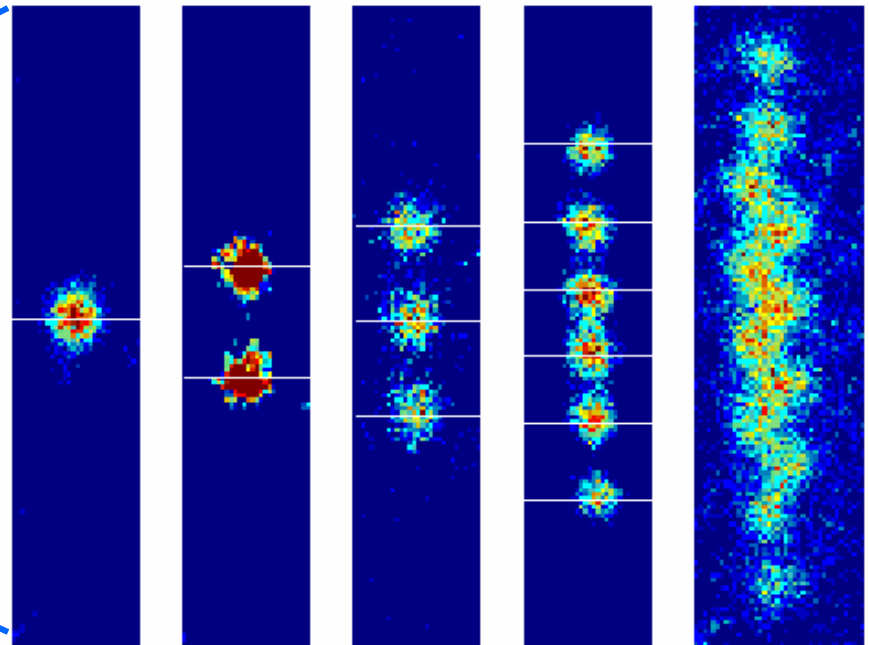
low pass filters

Planar Trap Chip

Magnified trap electrodes

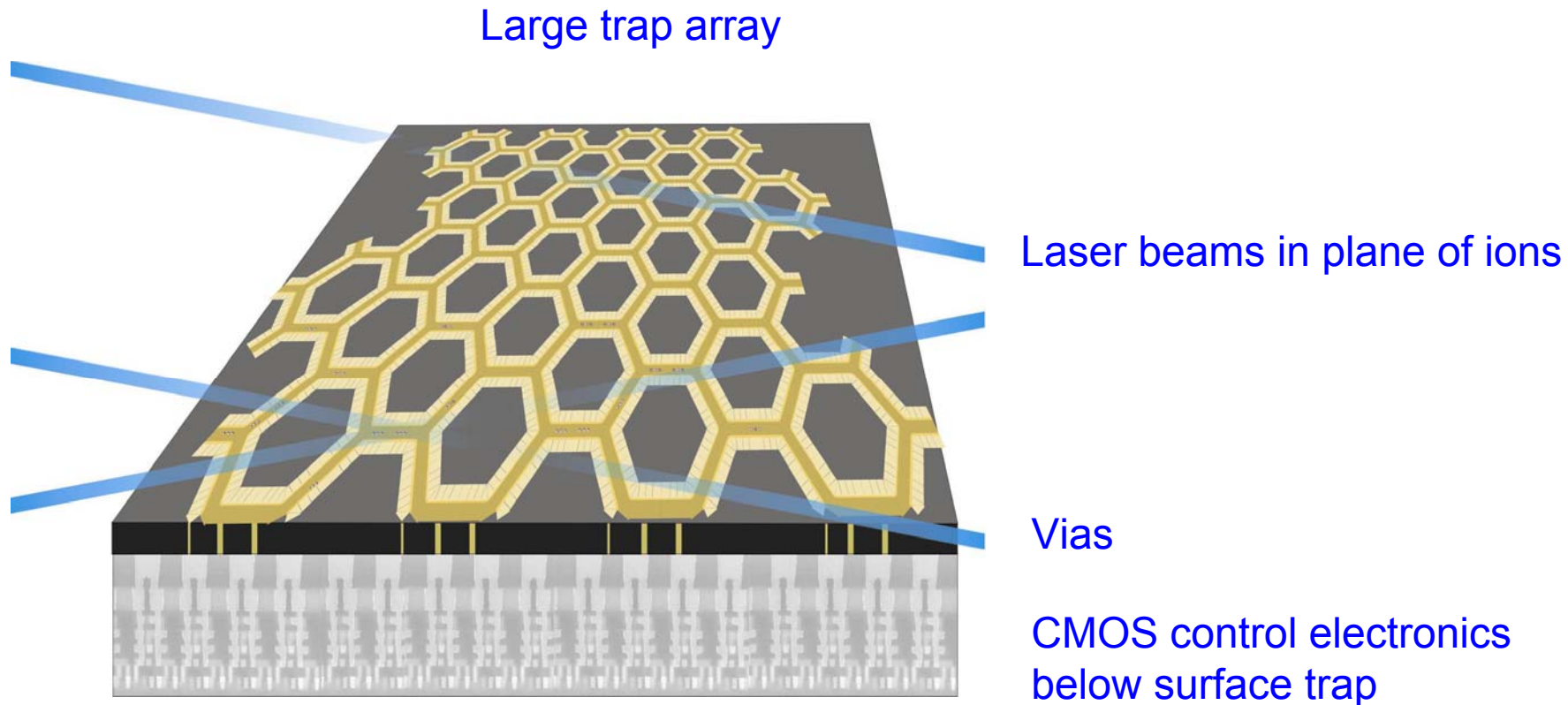


CCD pictures of strings of Mg^+ ions
(trapped 40 μm above surface)



Estimated heating rate < 10 quanta/ms

The future: Integrated ion chips ?



“Solid state” with the bulk separated from qubits

DTO/NSA Fabrication initiative:
Collaborations with Lucent Technology and Sandia Nat. Lab.

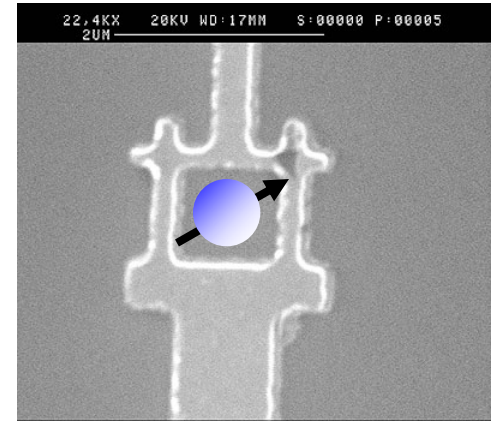
The future II: Hybrid systems ?

Ion/micro-cantilever:

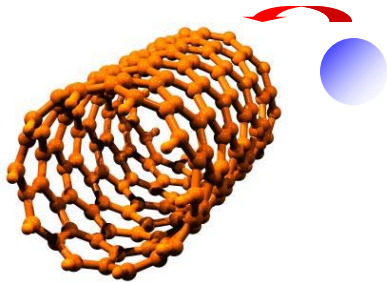


(Wineland et al., 1998)

Ion/Josephson junctions:



Ion/Carbon nanotubes:



(Tian, Zoller & Co. 2003)

Ion/(your micro-system here):



Cavity , microwave circuit ...

Multi-Qubit Algorithms

Towards Heisenberg-Limited Spectroscopy with Multiparticle Entangled States,
Science **304**, 1476 (2004).

Deterministic quantum teleportation of atomic qubits,
Nature **429**, 737 (2004).

Quantum Dense Coding with Atomic Qubits,
Phys. Rev. Lett. **93**, 040505 (2004).

Realization of quantum error correction,
Nature **432**, 603 (2004).

Enhanced Quantum State Detection Efficiency through Quantum Information Processing,
Phys. Rev. Lett. **94**, 010501 (2005).

Implementation of the Semiclassical Quantum Fourier Transform in a Scalable System,
Science **308**, 997 (2005).

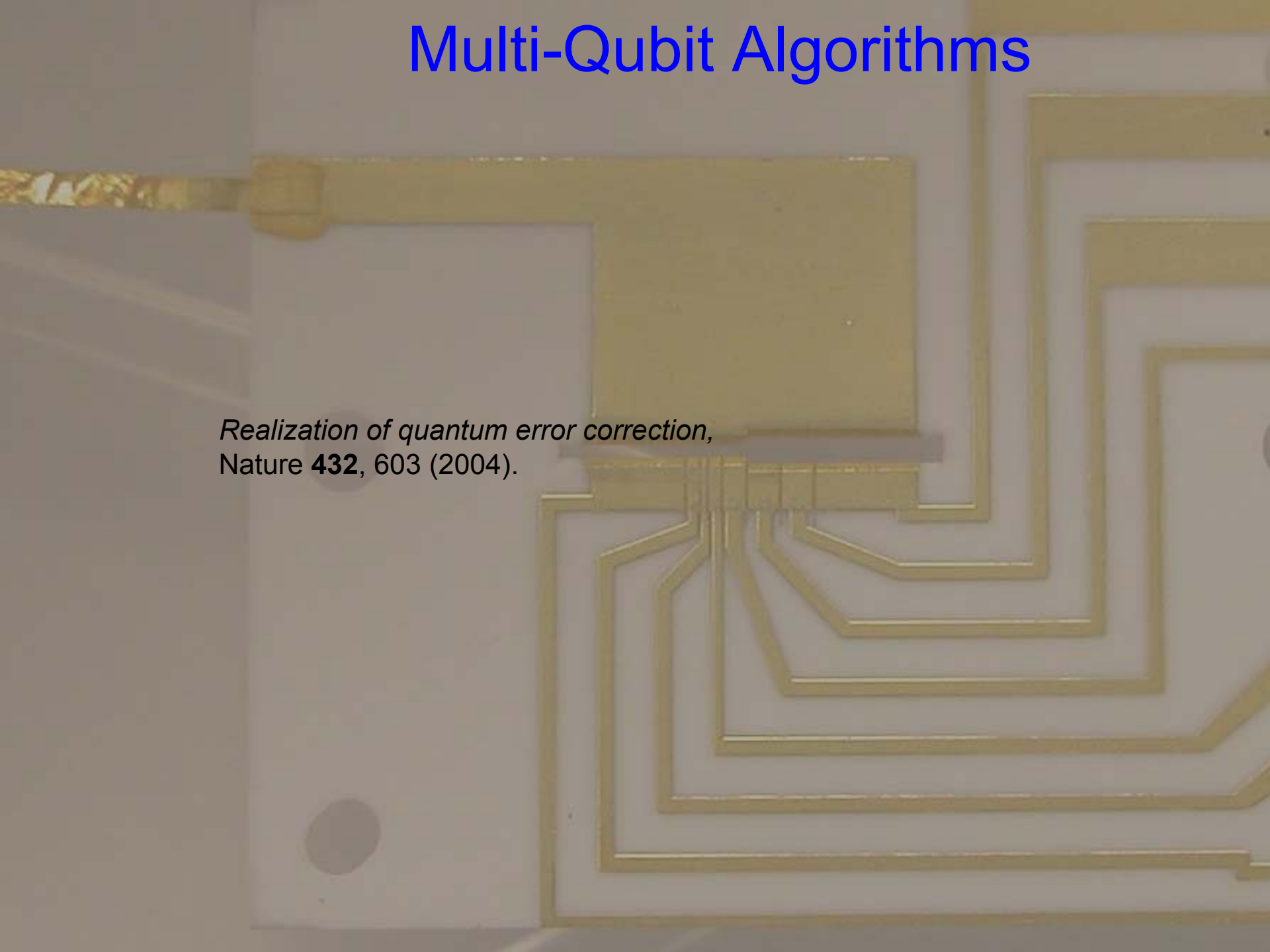
Hyperfine Coherence in the Presence of Spontaneous Photon Scattering,
Phys. Rev. Lett. **95**, 030403 (2005).

Long-lived qubit memory using atomic ions,
Phys. Rev. Lett. **95**, 060502 (2005).

Creation of a six-atom 'Schrödinger cat' state,
Nature **438**, 639 (2005).

Multi-Qubit Algorithms

Realization of quantum error correction,
Nature **432**, 603 (2004).



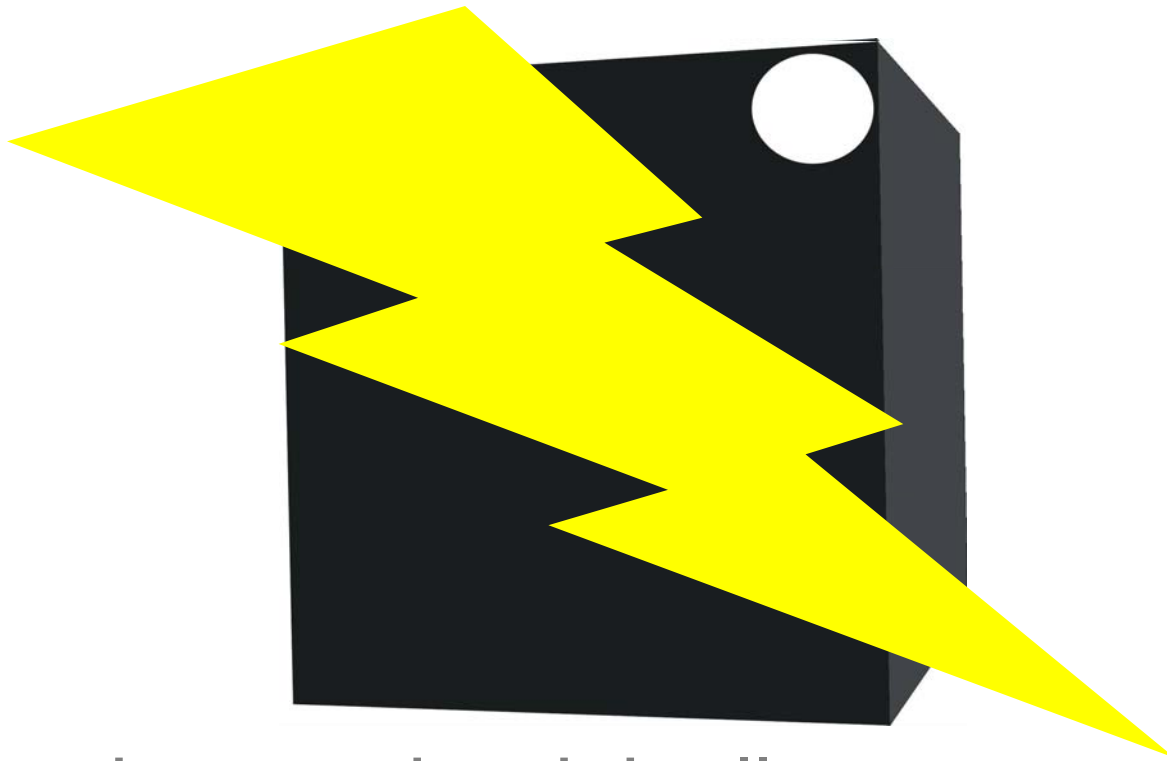
Why quantum error correction ?

Quantum error correction upgrades large scale quantum computing from
“Totally hopeless”
to
“Theoretically not hopeless”

Threshold theorem: If individual operation errors are sufficiently small
we can do arbitrarily long calculations.

Analogy for quantum error correction

How can one correct a qubit without looking at it?



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contained animated features

Analogy for quantum error correction

Ancilla measurement projects continuous errors to one of a few cases

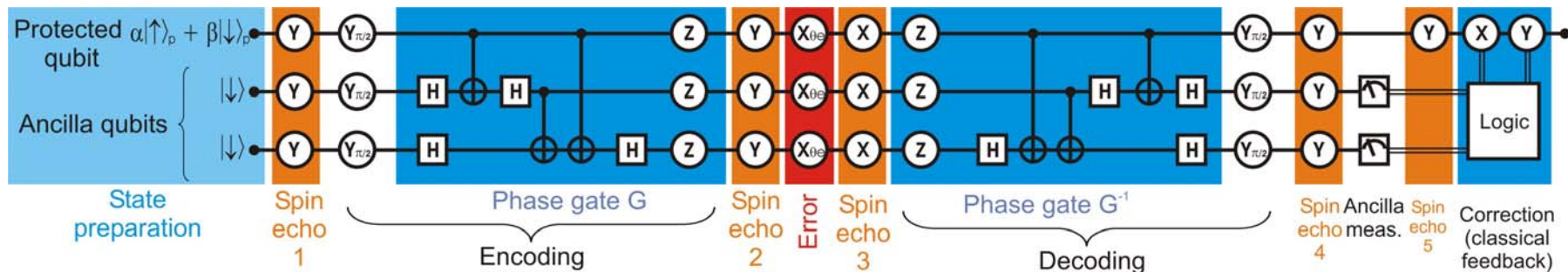
Measured ancilla values reveal necessary correction

Correction conditional on ancilla values repairs damage without ever looking at the qubit

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3 Qubit Bitflip Error-Correction

- Motivation: Scaling will require fault tolerance and therefore error correction
- experimental error correction with classical feedback from *measured* ancillas
 - based on new stabilizer code with generators $\{ZZX, ZXZ\}$ (no classical analog)

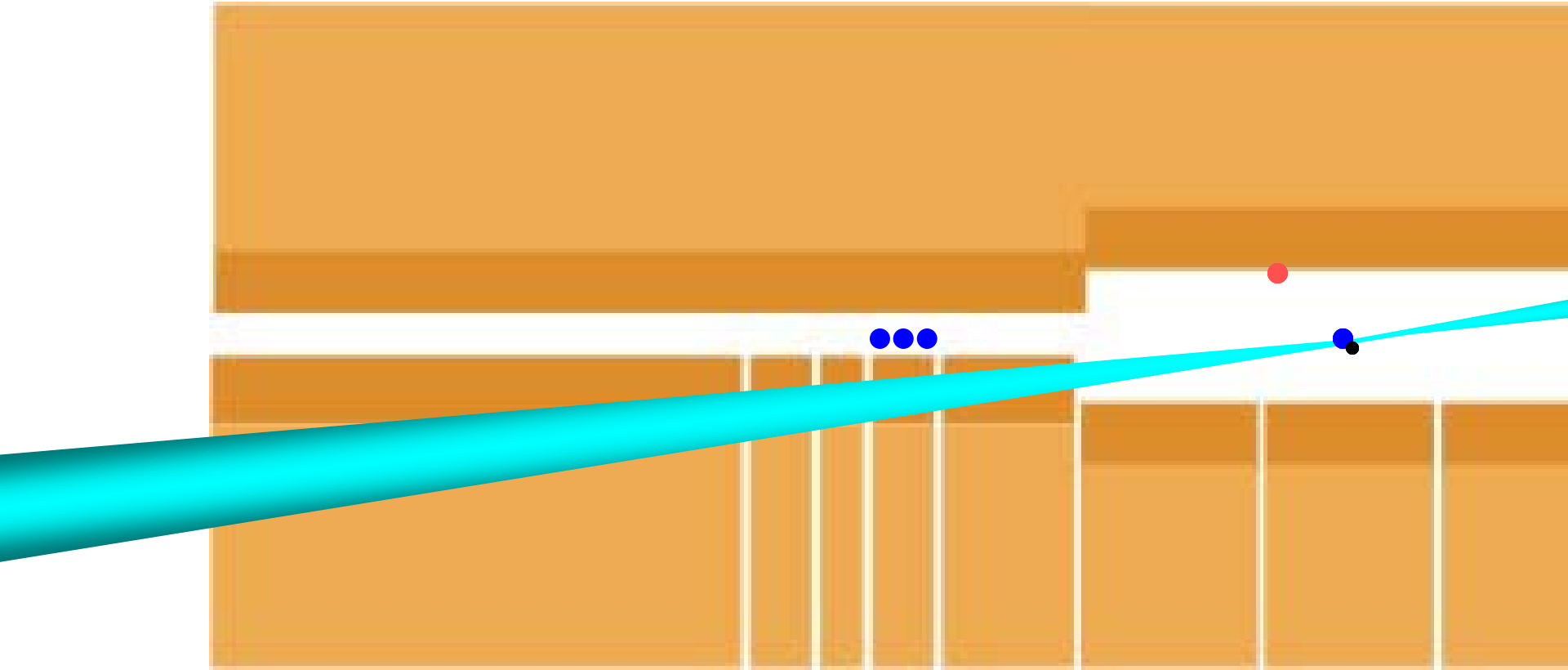


Quantum Error Correction



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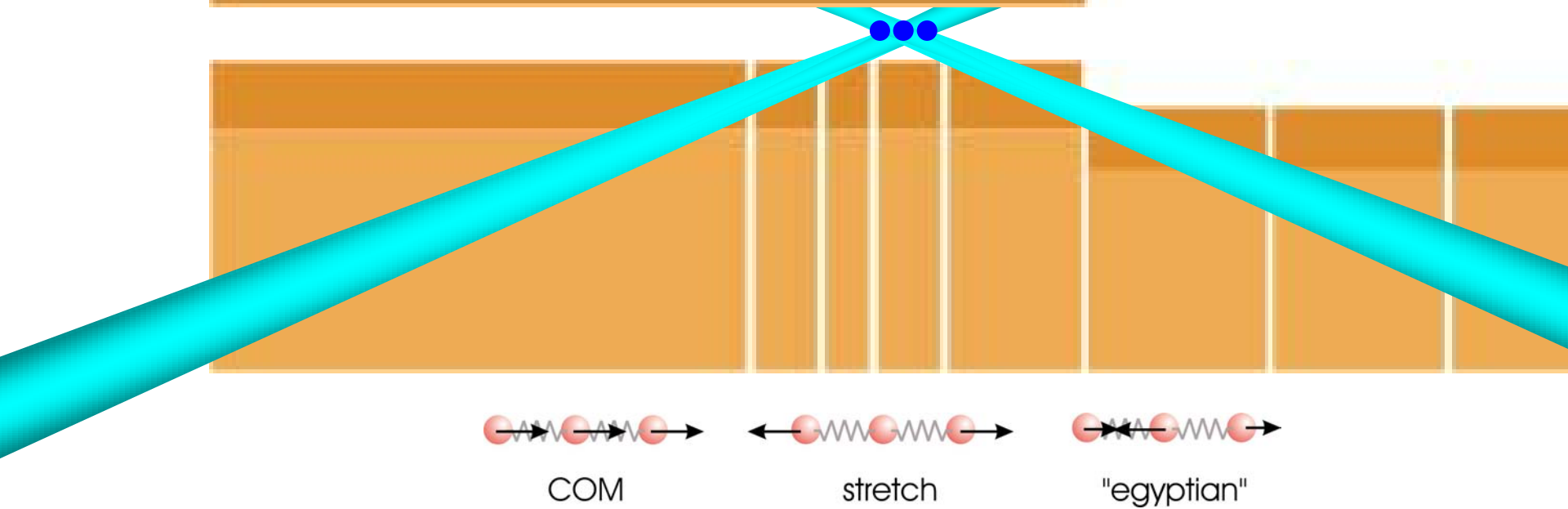
Loading the trap



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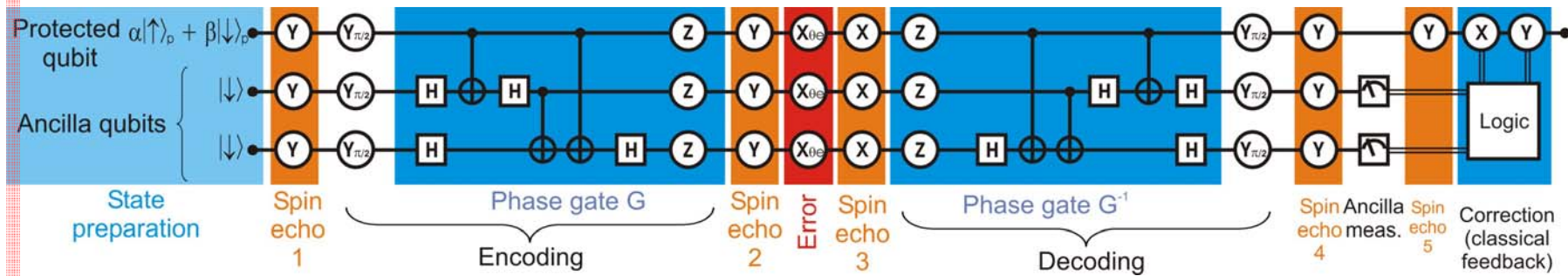
Ground state cooling

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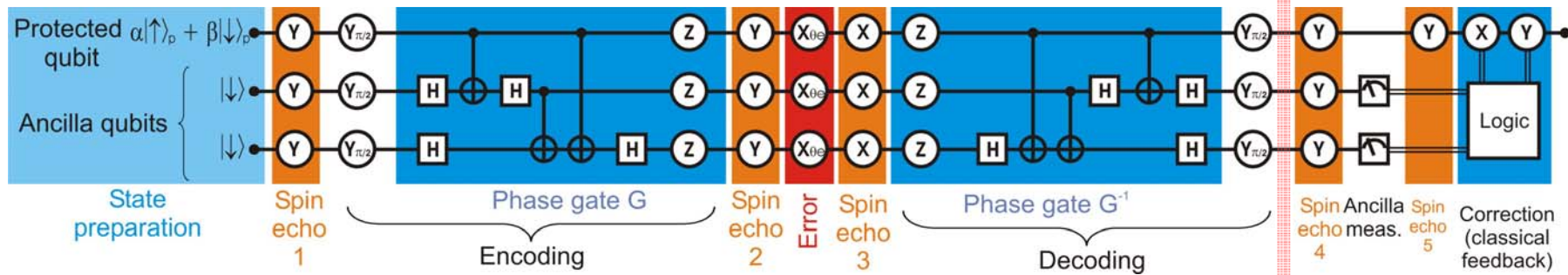
Quantum computer booted up !

Error correction implementation



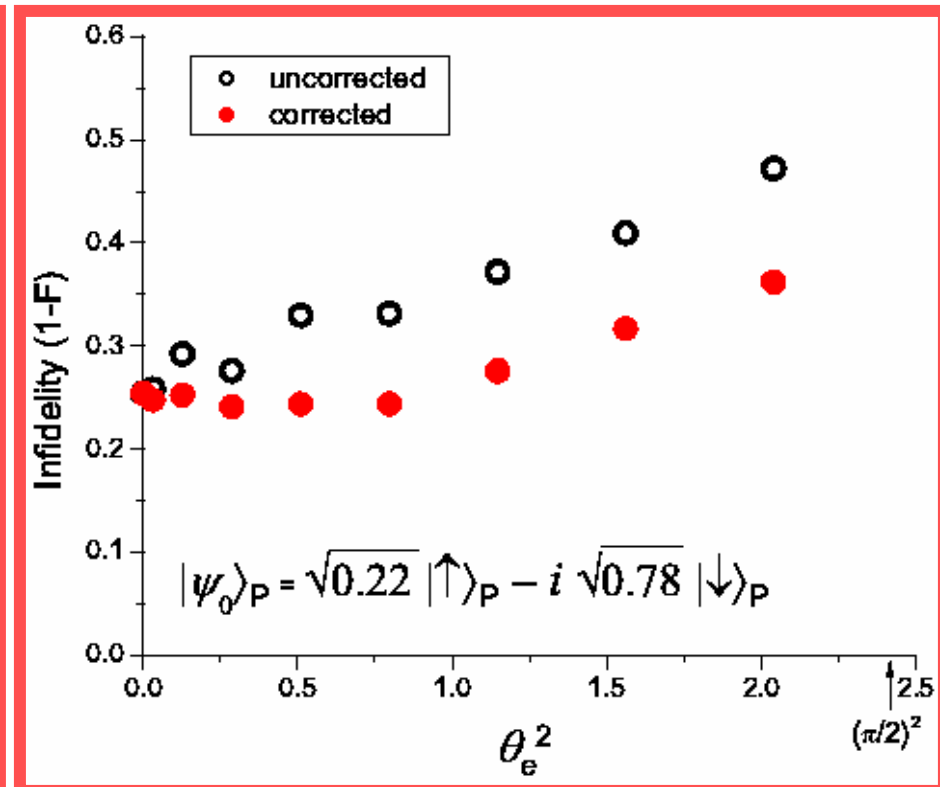
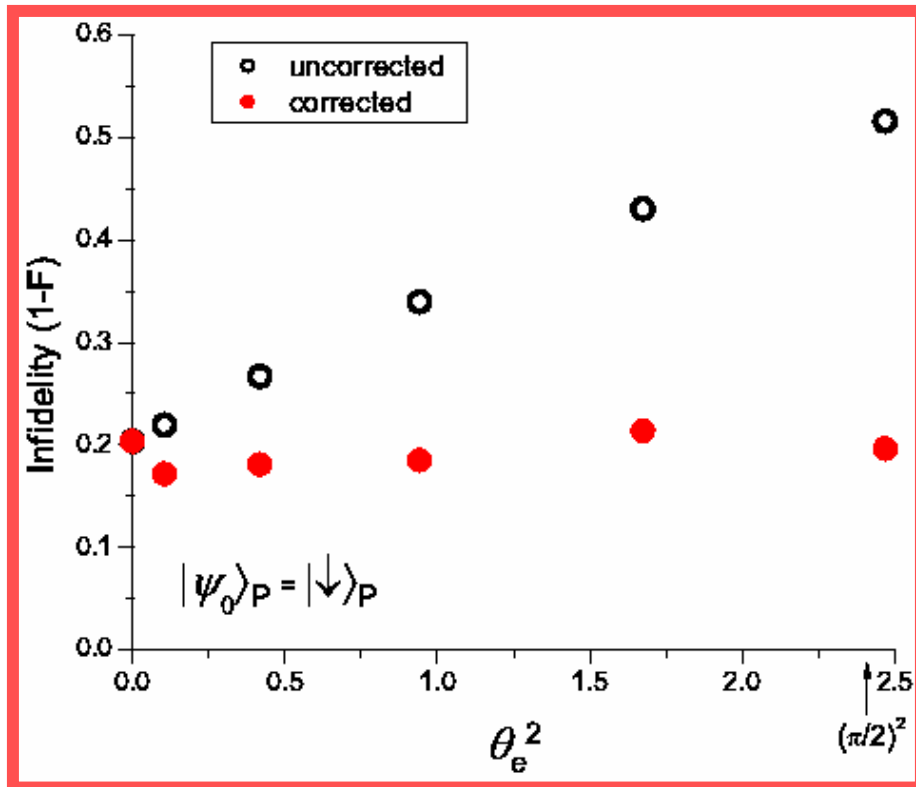
This viewgraph originally contained animated features

Error correction implementation



This viewgraph originally contained animated features

Error-Correction Results



- Uncorrected infid. $\sim \theta_e^2$, corrected infid. $\sim \theta_e^4$

Entanglement purification

Motivation: Distribute good entangled pairs for communication and computation.

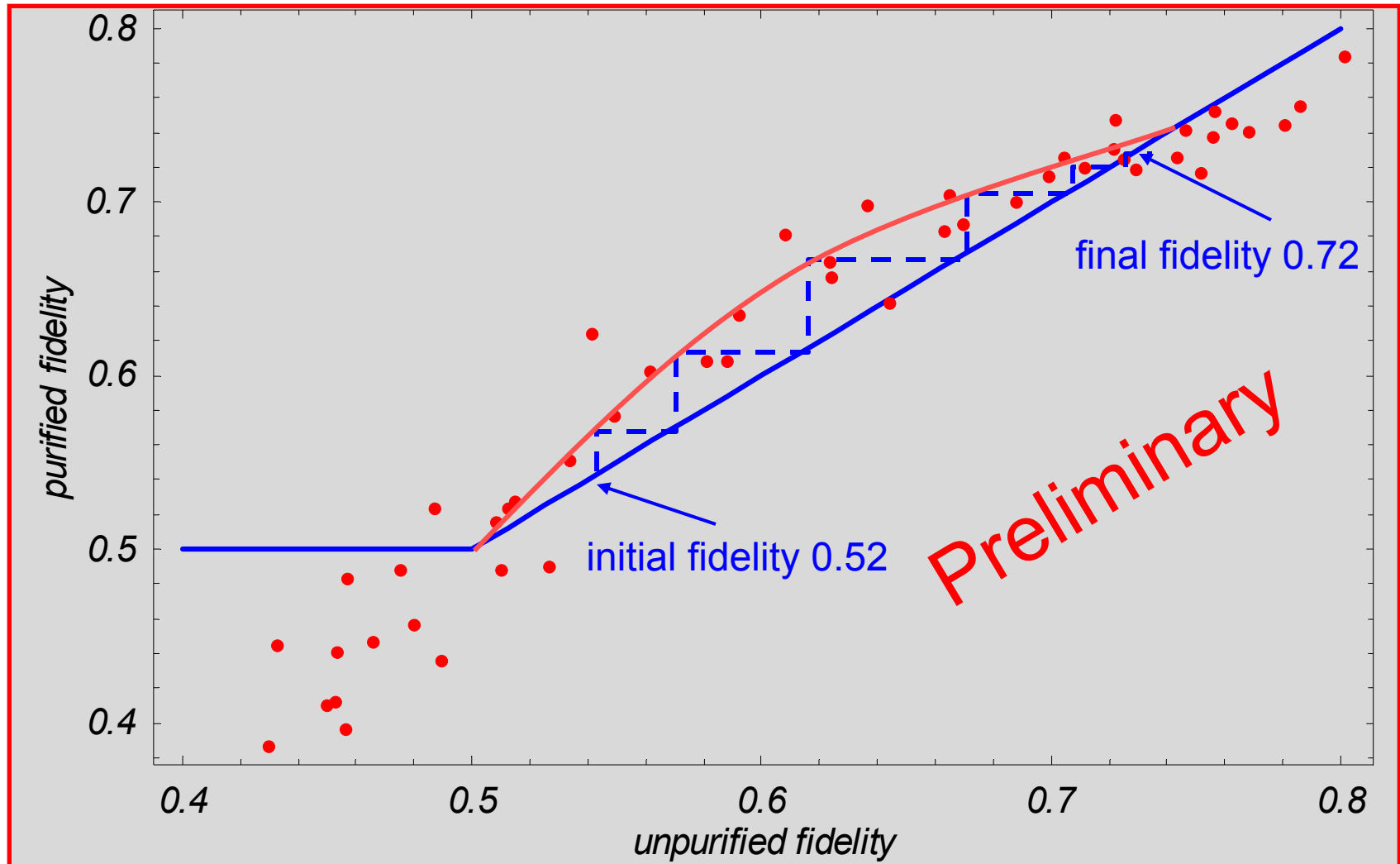
Increase maximum distance for quantum communication with modest effort (few qubit/few operations)

Goal: Produce one higher fidelity entangled pair out of two less entangled

Implementation (following C. Bennett et al.):

- Produce two entangled pairs
(two geometric phase gates)
- Do controlled rotations between parts of the pairs
(two geometric phase gates)
- Measure first pair, conditioned on outcome keep or discard first pair

Entanglement purification results



Gain would allow for entanglement pumping from $F=0.53$ to $F=0.75$

Present Status

DiVincenzo criteria demonstrated in individual experiments

Can control qubits with high operation fidelities
(97% two-qubit gate, 99% single qubit rotation)

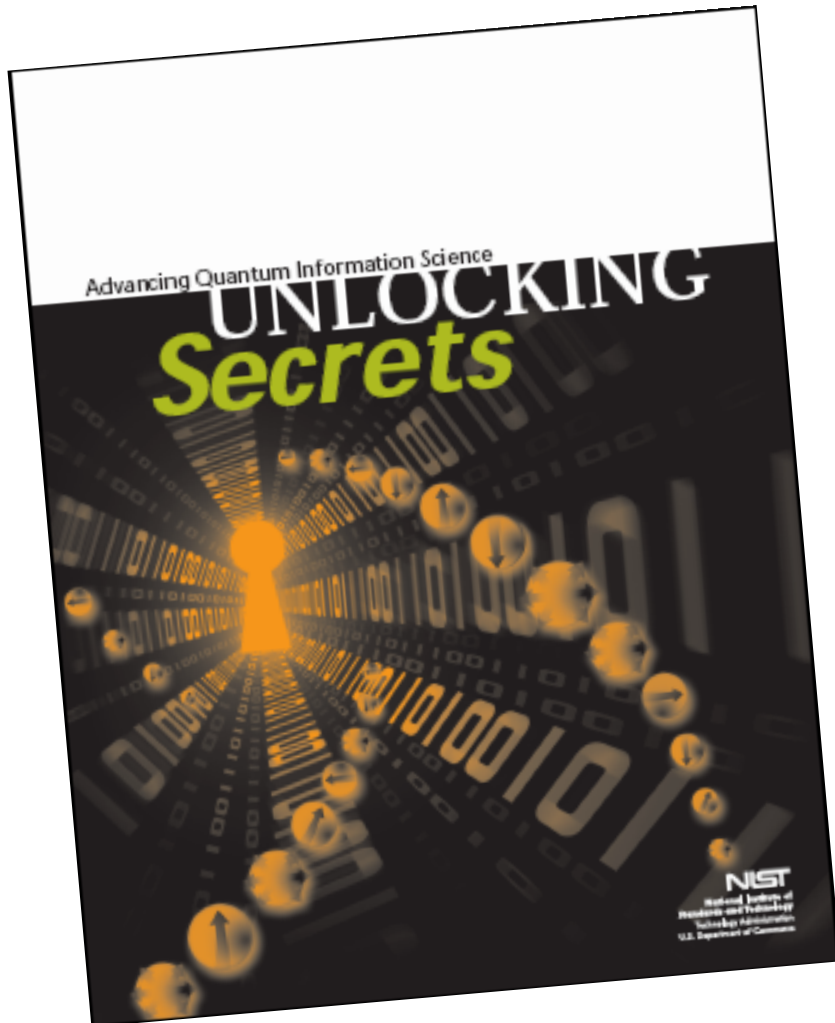
Field independent qubits reduce memory error significantly.
Dec. times $T_1 = \infty$; $T_2 > 10$ s.

Can do single qubit measurements (99.9% fidelity in 200 μ s) and feed back on system well within decoherence time.

Studies of multiplexed traps, surface traps and micro-fabrication (several groups).
Will try to scale to order 10 completely mobile qubits in the next 5 years

Achieving fault tolerance with high (0.9999) fidelity is *very hard* engineering job, but no known fundamental limits rule it out.

Quantum information at NIST



Further programs at NIST:

Neutral atoms (also Trey Porto's talk)

Quantum cryptography

Josephson junction qubits

Single photon resolving detectors

Overview in NIST brochure:

<http://qubit.nist.gov>